

**Notice of References Cited**

Application/Control No.

10/811,925

Applicant(s)/Patent Under  
Reexamination  
SHIBAYAMA ET AL.

Examiner

Todd Ingberg

Art Unit

2193

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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,658,578	12-2003	Laurenti et al.	713/324
*	B	US-4,977,582	12-1990	Nichols et al.	375/371
	C	US-			
	D	US-			
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	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Limits of PArallelism On Thread-Level Speculative Parallel Processing Architecture, Katsuhiko Metsugi et al, 6 pages, 2002
	V	NEC and Pacific Internet: Providing the Business Case for the IP Promise, NEC 2 pages, no date
	W	Modern Processor Design Fundamentals of Superscalar Processors, John Paul Shen et al, Beta Edition, October 9, 2002, Whole Book
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
 Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.